

APR 18 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: BUR920020055US1

In re Application of:

HAYES ET AL.

Serial No.: 10/604,107

Filed: 26 JUNE 2003

For: METHOD AND APPARATUS FOR
PERFORMING INPUT/OUTPUT FLOOR
PLANNING ON AN INTEGRATED
CIRCUIT DESIGN

Examiner: PIERRE LOUIS, A.

Art Unit: 2123

AMENDMENT UNDER 37 C.F.R. § 1.111MS Non-Final Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The present Amendment is submitted in reply to the Examiner's Office Action dated February 17, 2006, having a shortened statutory period set to expire May 17, 2006.

CERTIFICATE OF FACSIMILE
37 CFR § 1.8(a)

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date below.

4/18/06
DateLibby J. Ziegler
Signature